

REMARKS

Reconsideration of this application as amended is respectfully requested.

In the Office Action, claims 1-20 were pending. Claims 1-20 were rejected. In this response, no claim has been canceled. Claims 1, 9, 17, and 20 have been amended. New claims 21-24 have been added. Formal drawings have been submitted herein. No new matter has been added.

Claims 1-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 6,229,929 of Lynch et al. ("Lynch"). In view of the foregoing amendment, it is respectfully submitted that claims 1-20 include limitations that are not disclosed or suggested by Lynch.

Specifically, independent claim 1 recites as follows:

1. A method comprising:
receiving a sequence of image data to compress; and
specifying scalar quantization with a power of two step size using three bit values to apply to the sequence of image data, wherein the three bit values specify a number of bitplanes to be truncated during the quantization, and wherein a value with all three bits having an identical logical value indicates that all bitplanes are to be truncated.

(Emphasis added)

Independent claim 1 includes limitations that three-bit values (e.g., only using 3 bits) are used to specify the number of bitplanes to be truncated during the quantization, where a value with all three bits having the same logical values (e.g., a logical value of one) indicates that all bitplanes are to be truncated. For example, three-bit values having from 0-6 indicate 0-6 bitplanes to be truncated respectively, while a value of 7 (e.g., all three bits have a logical value of 1) indicates that all bitplanes (which may be more than 6 bitplanes) to be truncated. It is respectfully submitted that the above limitations are absent from Lynch.

Although Lynch indicated that certain LSB bits of coefficients are to be truncated in a power of two step, it is unclear how Lynch specifies the number of bits to be truncated. Specifically, Lynch fails to disclose that the number of the bitplanes that may be translated into number of bits in power of two step is specified using a value only containing three bits, where a value having all bits as the same logical value indicates that all bitplanes are to be truncated. Therefore, it is respectfully submitted that independent claim 1 is patentable over Lynch.

Similarly, independent claims 9, 17, and 20 include limitations similar to those recited in claim 1. Thus, for the reasons similar to those discussed above, independent claims 9, 17, and 20 are patentable over Lynch.

Given that the rest of the claims depend from one of the above independent claims, at least for the reasons similar to those discussed above, it is respectfully submitted that the rest of the claims are patentable over Lynch. Withdrawal of the rejections is respectfully requested.

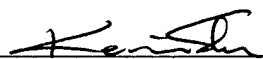
In view of the foregoing, Applicants respectfully submit the present application is now in condition for allowance. If the Examiner believes a telephone conference would expedite or assist in the allowance of the present application, the Examiner is invited to call the undersigned attorney at (408) 720-8300.

Please charge Deposit Account No. 02-2666 for any shortage of fees in connection with this response.

Respectfully submitted,

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